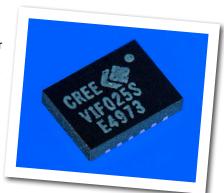


CGHV1F025S

25 W, DC - 15 GHz, 40V, GaN HEMT

Cree's CGHV1F025S is an unmatched, gallium nitride (GaN) high electron mobility transistor (HEMT) designed specifically for high efficiency, high gain and wide bandwidth capabilities. The device can be deployed for L, S, C, X and Ku-Band amplifier applications. The datasheet specifications are based on a X-Band (8.9 - 9.6 GHz) amplifier. The CGHV1F025S operates on a 40 volt rail circuit while housed in a 3mm x 4mm, surface mount, dual-flat-no-lead (DFN) package. Under reduced power, the transistor can operate below 40V to as low as $20VV_{DD}$ maintaining high gain and efficiency.



Package Type: 3x4 DFN PN: CGHV1F025S

Typical Performance 8.9 - 9.6 GHz ($T_c = 25^{\circ}C$), 40 V

Parameter	8.9 GHz	9.2 GHz	9.4 GHz	9.6 GHz	Units
Output Power @ P _{IN} = 37 dBm	24	29	27	25	W
Drain Efficiency @ P _{IN} = 37 dBm	43.5	48.5	48	46	%
Gain @ P _{IN} = 0 dBm	10.7	11.6	11.3	11.1	dB

Note:

Measured in the CGHV1F025S-AMP1 application circuit. Pulsed 100 µs 10% duty.

Features

- Up to 15 GHz Operation
- 25 W Typical Output Power
- 11 dB Gain at 9.4 GHz
- Application circuit for 8.9 9.6 GHz



Absolute Maximum Ratings (not simultaneous) at 25°C Case Temperature

Parameter	Symbol	Rating	Units	Notes
Drain-Source Voltage	$V_{\scriptscriptstyle DSS}$	100	Volts	25°C
Gate-to-Source Voltage	$V_{\sf GS}$	-10, +2	Volts	25°C
Storage Temperature	T_{STG}	-65, +150	°C	
Operating Junction Temperature	$T_{\!\scriptscriptstyle J}$	225	°C	
Maximum Forward Gate Current	I _{GMAX}	4.8	mA	25°C
Maximum Drain Current ¹	I _{DMAX}	2	Α	25°C
Soldering Temperature ²	T_s	245	°C	
Case Operating Temperature ^{3,4}	T _c	-40, +150	°C	
Thermal Resistance, Junction to Case ⁵	$R_{\theta JC}$	3.4	°C/W	85°C

Note:

Electrical Characteristics (T_c = 25°C) - 40 V Typical

Characteristics	Symbol	Min.	Тур.	Max.	Units	Conditions
DC Characteristics ¹						
Gate Threshold Voltage	$V_{\rm GS(th)}$	-3.8	-3.0	-2.3	V _{DC}	V _{DS} = 10 V, I _D = 4.8 mA
Gate Quiescent Voltage	$V_{_{\mathrm{GS}(\mathbb{Q})}}$	-	-2.7	-	V _{DC}	V _{DS} = 40 V, I _D = 240 mA
Saturated Drain Current ²	I _{DS}	3.8	4.3	-	Α	$V_{DS} = 6.0 \text{ V}, V_{GS} = 2.0 \text{ V}$
Drain-Source Breakdown Voltage	V _{(BR)DSS}	100	-	-	V _{DC}	$V_{GS} = -8 \text{ V, I}_{D} = 4.8 \text{ mA}$
RF Characteristics 3 (T _c = 25 $^{\circ}$ C, F ₀ = 6.0 GHz un	less otherwis	e noted)				
Gain	G	-	16	-	dB	V _{DD} = 40 V, I _{DQ} = 150 mA, P _{IN} = 0 dBm
Output Power ⁴	P _{out}	-	29	-	W	V _{DD} = 40 V, I _{DQ} = 150 mA, P _{IN} = 34 dBm
Drain Efficiency⁴	η	-	55	-	%	$V_{DD} = 40 \text{ V, } I_{DQ} = 150 \text{ mA, } P_{IN} = 34 \text{ dBm}$
Output Mismatch Stress ⁴	VSWR	-	10:1	-	Υ	No damage at all phase angles, $V_{DD} = 40 \text{ V, I}_{DQ} = 150 \text{ mA, P}_{OUT} = 29 \text{ W}$
Dynamic Characteristics						
Input Capacitance ⁵	C _{gs}	-	5.9	-	pF	$V_{DS} = 40 \text{ V, } V_{gs} = -8 \text{ V, f} = 1 \text{ MHz}$
Output Capacitance ⁵	C _{DS}	-	2	-	pF	$V_{DS} = 40 \text{ V, } V_{gs} = -8 \text{ V, f} = 1 \text{ MHz}$
Feedback Capacitance	C_{GD}	-	0.21	-	pF	$V_{DS} = 40 \text{ V, } V_{gs} = -8 \text{ V, } f = 1 \text{ MHz}$

Notes:

¹ Current limit for long term, reliable operation

² Refer to the Application Note on soldering at www.cree.com/rf/document-library

 $^{^3}$ Simulated at P_{DISS} = 2.4 W

 $^{{}^4}T_c$ = Case temperature for the device. It refers to the temperature at the ground tab underneath the package. The PCB will add additional thermal resistance.

⁵ Pulsed (100 μs, 10% Duty). Rth for Cree's reference design using a 10 mil Rogers 5880 PCB with 31 (Ø13 mil) Vias would be 3.6 °C/W. For CW operation, the Rth numbers increase to 5°C/W for just the device, and 7.3 °C/W including the board.

¹ Measured on wafer prior to packaging

² Scaled from PCM data

³ Measured in CGHV1F025S-AMP

⁴ Pulsed 100 μs, 10% duty cycle

⁵ Includes package



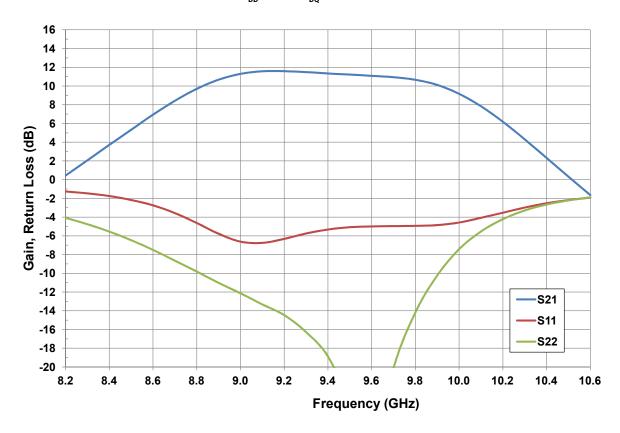
Electrical Characteristics When Tested in CGHV1F025S-AMP1

Characteristics	Symbol	Min.	Тур.	Max.	Units	Conditions
RF Characteristics ¹ (T _c = 25°C, F ₀ = 8.9 - 9.6 GHz unless otherwise noted)						
Gain	G	-	11.6	-	dB	$V_{DD} = 40 \text{ V, } I_{DQ} = 150 \text{ mA, } P_{IN} = 0 \text{ dBm}$
Output Power ²	P _{out}	-	29	-	W	$V_{DD} = 40 \text{ V, } I_{DQ} = 150 \text{ mA, } P_{IN} = 37 \text{ dBm}$
Drain Efficiency ²	η	-	48.5	-	%	$V_{\rm DD}$ = 40 V, $I_{\rm DQ}$ = 150 mA, $P_{\rm IN}$ = 37 dBm
Output Mismatch Stress ²	VSWR	-	10:1	-	Υ	$V_{\rm DS}$ = 40 V, $V_{\rm gs}$ = -8 V, $P_{\rm OUT}$ = 25 W

Notes:

Typical Performance - CGHV1F025S-AMP1

Figure 1. - Typical Small Signal Response of CGHV1F025S-AMP1 Application Circuit $V_{\rm DD}$ = 40 V, $I_{\rm DO}$ = 150 mA



¹ Measured in CGHV1F025S-AMP1 Application Circuit

² Pulsed 100 μs, 10% duty cycle



Typical Performance in Application Circuit CGHV1F025S-AMP1

Figure 2. - Typical Large Signal Response V_{DD} = 40 V, I_{DQ} = 150 mA, P_{IN} = 37 dBm Tcase = 25°C, Pulse Width = 100 μ s, Duty Cycle = 10 %

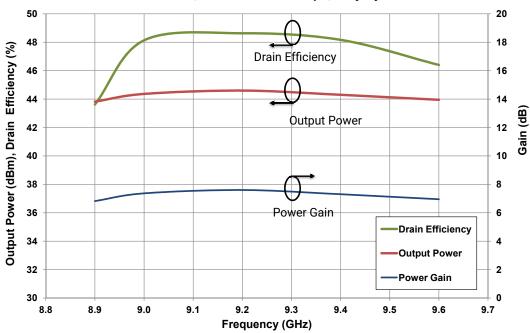
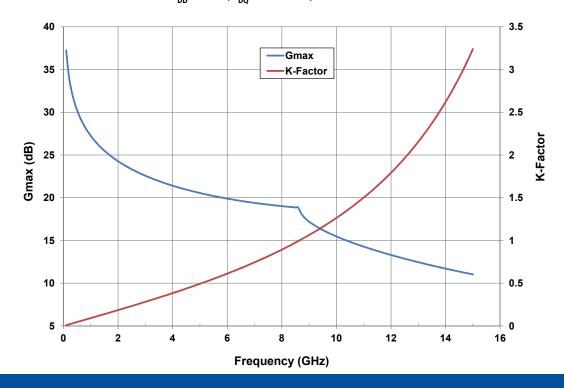


Figure 3. - G_{MAX} and K-Factor vs Frequency V_{DD} = 40 V, I_{DO} = 150 mA, Tcase = 25°C

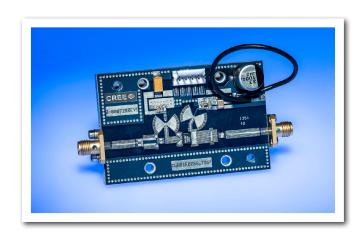




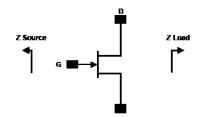
CGHV1F025S-AMP1 Application Circuit Bill of Materials

Designator	Description	Qty
R1	RES, 100, OHM, +1/-1%, 1/16 W, 0603	1
R2	RES, 10, OHM, +1/-1%, 1/16 W, 0603	1
C1, C2	CAP, 1pF, ±0.1 pF, 0603, ATC	2
C3, C4	CAP, 1.8pF, ±0.1 pF, 0603, ATC	2
C9, C10	CAP, 0.6pF, ±0.1 pF, 0603, ATC	2
C5, C11	CAP, 10 pF, ±5%, 0603, ATC	1
C6, C12	CAP, 470 pF, 5%, 100 V, 0603, X	2
C7, C13	CAP, 33000 pF, 0805, 100V, X7R	2
C14	CAP, 1.0 UF, 100V, 10%, X7R, 1210	1
C8	CAP, 10 UF, 16V TANTALUM	1
C15	CAP, 33UF, 20%, G CASE	1
J1, J2	CONN, SMA, PANEL MOUNT JACK, FLANGE	2
J3	HEADER RT>PLZ .1CEN LK 5POS	1
Q1	QFN TRANSISTOR CGHV1F025S	1
W1	CABLE, 18 AWG, 4.2	1
	Rogers 5880 PCB 10 mils	1

CGHV1F025S-AMP1 Application Circuit



Source and Load Impedances



Frequency (MHz)	Z Source	Z Load
700	0.88 - j9.7	3.48 - j2.14
800	1.07 - j12.1	3.07 - j4.13
900	1.12 - j14.74	2.87 - j6.82
1000	1.15 - j17.64	2.49 - j9.63
1100	1.26 - j20.96	1.75 - j12.30

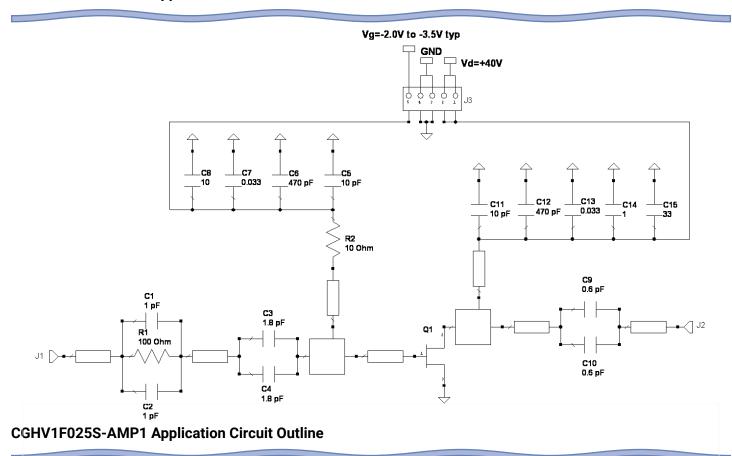
Note: $V_{DD} = 40 \text{ V}$, $I_{DQ} = 150 \text{ mA}$ in the 3x4 DFN package.

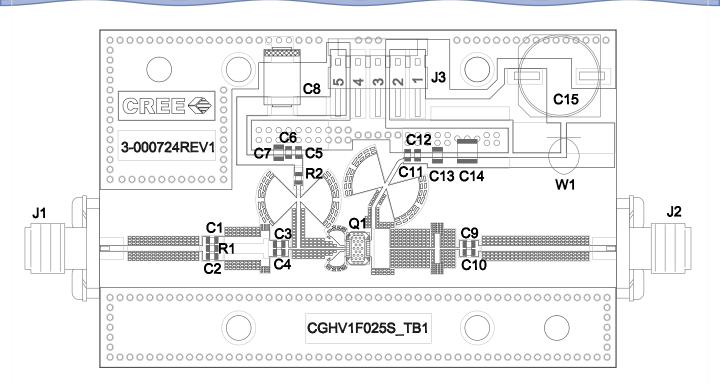
Electrostatic Discharge (ESD) Classifications

Parameter	Symbol	Class	Test Methodology
Human Body Model	НВМ	1A (> 250 V)	JEDEC JESD22 A114-D
Charge Device Model	CDM	2 (125 V to 250 V)	JEDEC JESD22 C101-C



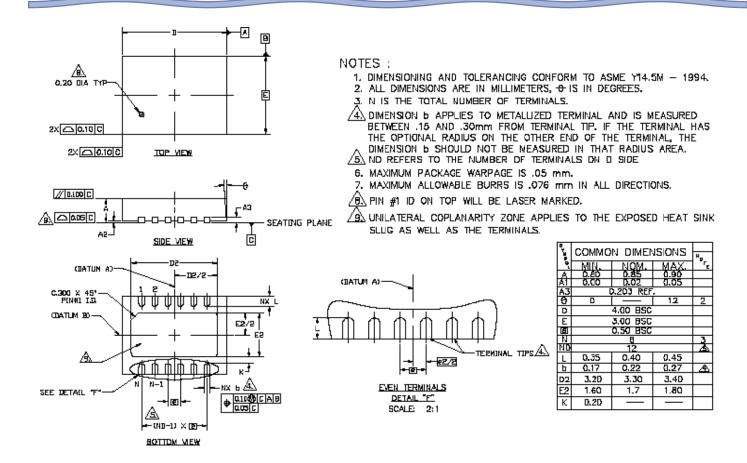
CGHV1F025S-AMP1 Application Circuit Schematic



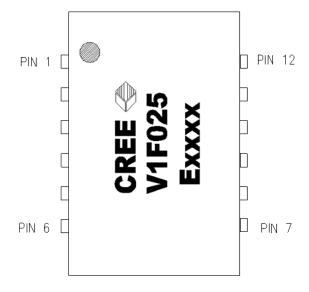




Product Dimensions CGHV1F025S (Package 3 x 4 DFN)



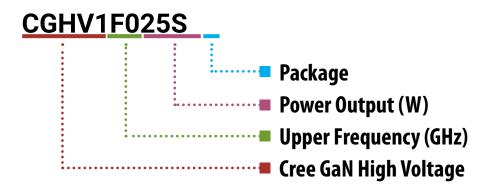
Pin	Input/Output
1	GND
2	RF IN
3	RF IN
4	RF IN
5	RF IN
6	GND
7	GND
8	RF OUT
9	RF OUT
10	RF OUT
11	RF OUT
12	GND



Note: Leadframe finish for 3x4 DFN package is Nickel/Palladium/Gold. Gold is the outer layer.



Part Number System



Parameter	Value	Units
Upper Frequency ¹	15.0	GHz
Power Output	25	W
Package	Surface Mount	-

Table 1.

Note¹: Alpha characters used in frequency code indicate a value greater than 9.9 GHz. See Table 2 for value.

Character Code	Code Value
А	0
В	1
С	2
D	3
E	4
F	5
G	6
Н	7
J	8
K	9
Examples:	1A = 10.0 GHz 2H = 27.0 GHz

Table 2.



Product Ordering Information

	<u></u>		
Order Number CGHV1F025S	Description GaN HEMT	Unit of Measure Each	Image CHILLEGE 13
CGHV1F025-AMP1	Test board with GaN HEMT installed	Each	ORES DE LINE DE LA CONTROL DE
CGHV1F025S-TR	Delivered in Tape and Reel	250 parts / reel	



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