# Tantalum Surface Mount Capacitors – High Temperature T500 Series MnO<sub>2</sub> 200°C

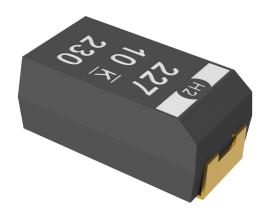


#### **Overview**

The KEMET T500 Series is a high-temperature product that offers optimum performance characteristics in applications with operating temperatures up to 200°C. This series is classified as MSL (Moisture Sensitivity Level) 1 under J STD 020: unlimited floor life time at ≤30°C/85% RH. \*Due to the potential use of high melting point solders, KEMET has taken the initiative to package this series in moisture barrier bags with desiccant and a humidity indicator card.

#### **Benefits**

- Meets or exceeds EIA standard 535BAAC
- · Weibull failure rate to B Level available
- · Standard gold-plated termination
- RoHS Compliant
- Operating temperature range of -55°C to +200°C
- 100% steady-state accelerated aging at 200°C
- Voltage derating is 1/3 at 200°C
- + Qualified at 1,000 hours of life test at 200°C with 0.33  $\rm V_{\scriptscriptstyle R}$
- Taped and reeled per EIA 481
- Meets MSL 1 requirements for Pb-free assembly according to JEDEC J-STD-020
- Surge current options available



### **Applications**

Typical applications include decoupling and filtering in down-hole, military and aerospace industries.

#### **K-SIM**

For a detailed analysis of specific part numbers, please visit ksim.kemet.com to access KEMET's K-SIM software. KEMET K-SIM is designed to simulate behavior of components with respect to frequency, ambient temperature, and DC bias levels.



# **Ordering Information**

Т	500	X	227	Μ	010	Α	G	61	10
Capacitor Class	Series	Case Size	Capacitance Code (pF)	Capacitance Tolerance	Rated Voltage (VDC)	Failure Rate/ Design	Termination Finish	Performance	ESR
T = Tantalum	High Temperature 200°C	X	First two digits represent significant figures. Third digit specifies number of zeros.	K = ±10% M = ±20%	010 = 10 016 = 16 035 = 35	A = N/A B= 0.1%/1,000 hours	G = Gold plated	61 = Surge None 62 = Surge at 25°C after Weibull 63 = Surge -55°C and +85°C after Weibull	10 = Standard ESR

### **Performance Characteristics**

Item	Performance Characteristics
Operating Temperature	-55°C to 200°C
Rated Capacitance Range	33 – 220 μF at 120 Hz/25°C
Capacitance Tolerance	K Tolerance (10%), M Tolerance (20%)
Rated Voltage Range	10 – 35 V
DF (120 Hz)	Refer to Part Number Electrical Specification Table
ESR (100 kHz)	Refer to Part Number Electrical Specification Table
Leakage Current	$\leq$ 0.01 CV (µA) at rated voltage after 5 minutes



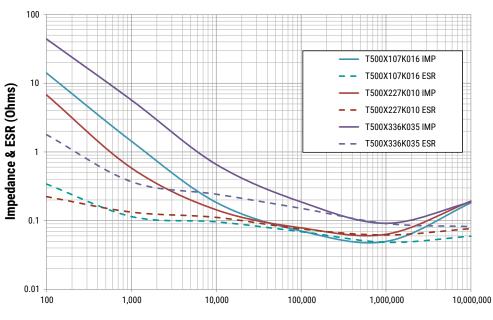
# Qualification

Test	Condition			Characteristics				
			Δ C/C	Within ±10%	6 of initial valu	е		
<b>F</b> adamas	000%0 -+ 1/0 + + + 1 000 h		DF	Within initial limits				
Endurance	200°C at 1/3 rated voltage, 1,000 hours		DCL	1 mAmp ma	aximum			
		ESR	Within initia	al limits				
			Δ C/C	Within ±10%	6 of initial valu	е		
0	000%0 st 0 lts 1 000 h sum		DF	Within initia	al limits			
Storage Life	200°C at 0 volts, 1,000 hours		DCL	1 mAmp maximum				
		ESR	Within initia	al limits				
		ΔC/C	Within ±10%	6 of initial valu	e			
		DF	Within initia	al limits				
Humidity	85°C, 85% RH, 0 V, 1,000 hours	DCL	Within initial limits					
		ESR	Within initial limits					
			+25°C	-55°C	+85°C	+150°C		
T 04. 1 'l''	Extreme temperature exposure at a	ΔC/C	IL*	±10%	±10%	±20%		
Temperature Stability	succession of continuous steps at +25°C, -55°C, +25°C, +85°C, +125°C, +25°C	DF	IL	IL	1.5 x IL	1.5 x IL		
		DCL	IL	N/A	10 x IL	12 x IL		
	MIL-STD-202, Method 213, Condition I, 100	Gneak	ΔC/C	Within ±10 of initial value				
Mechanical Shock/ Vibration	MIL-STD-202, Method 204, 10 Hz to 2,000 I	DF	Within initia	Within initial limits				
Vibration	20 minutes, 12 cycles each of 3 orientations		DCL	Within initia	al limits			

\*IL = Initial limit



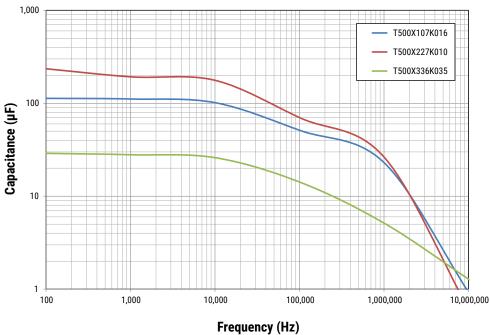
### **Electrical Characteristics**



#### Impedance & ESR vs. Frequency

Frequency (Hz)

The measurements were taken at room temperature (25°C)

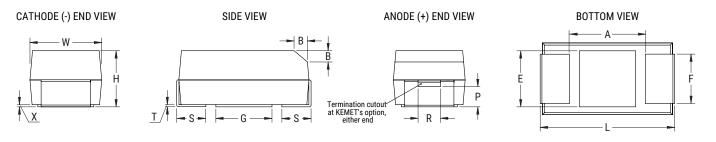


#### Capacitance vs. Frequency

The measurements were taken at room temperature (25°C)



#### **Dimensions – Millimeters**



Case	Size		Component											
KEMET	EIA	L	W	Н	F ±0.1 ±(0.004)		B ±0.15 (Ref) ±0.006	X (Ref)	P (Ref)	R (Ref)	T (Ref)	A (Min)	G (Ref)	E (Ref)
х	7343-43	7.3±0.3 (0.287±0.012)	4.3±0.3 (0.169±0.012)	4.0±0.3 (0.157±0.012)	2.4 (0.095)	1.3 (0.051)	0.5 (0.020)	0.10±0.10 (0.004±0.004)	1.7 (0.067)	1.0 (0.039)	0.13 (0.005)	3.8 (0.150)	3.5 (0.138)	3.5 (0.138)

Notes: (Ref) – Dimensions provided for reference only.

### Table 1 – Ratings & Part Number Reference

Rated Voltage		king tage	Rated Cap	Case Code/ Case Size	KEMET Part Number	DC Leakage		DF	ESR	Maximum Allowable Ripple Current		ipple	Maximum Operating Temp	MSL
VDC at 85°C		VDC at +200°C	μF	KEMET/EIA	(See below for part options)	µA at 20°C Max/5 Min.	µA at 200°C, 0.33 V <sub>R</sub> Max/5 Min.	% at 20°C 120 Hz Max.	100 kHz		mA at +125°C 100 kHz		°C	Reflow Temp ≤ 260°C
10	6.6	3.3	220	X/7343-43	T500X227(1)010(2)G(3)10	22	220	10	250	812	325	81	200	1
16	10.6	5.3	100	X/7343-43	T500X107(1)016(2)G(3)10	16	160	8	250	812	325	81	200	1
35	23.1	11.6	10	X/7343-43	T500X106(1)035(2)G(3)10	3.5	35	6	700	486	194	49	200	1
35	23.1	11.6	33	X/7343-43	T500X336(1)035(2)G(3)10	11.6	116	8	600	524	210	52	200	1

(1) To complete KEMET part number, insert M for  $\pm 20\%$  or K for  $\pm 10\%$ . Designates capacitance tolerance.

(2) To complete KEMET part number, insert B (0.1%/1,000 hours) or A = N/A. Designates reliability level.

(3) To complete KEMET part number, insert 61 = None, 62 = 10 cycles +25°C after Weibull, 63 = 10 cycles -55°C +85°C after Weibull. Designates surge current option.

Refer to Ordering Information for additional detail.

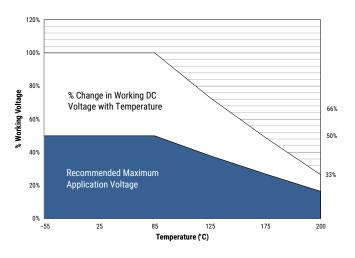
Better than series product may be substituted within the same capacitance and voltage at KEMET's option.



### **Recommended Voltage Derating Guidelines**

Rated Voltage	W	orking	Volta	ge	Recommended Application Voltage (for maximum reliability)				
	+25°C	+85°C	+125°C	+200°C	25°C	85°C	125°C	200°C	
10	10	10	6.6	3.3	5	5	3.3	1.7	
16	16	16	10.6	5.3	8	8	5.3	2.6	
35	35	35	23.1	11.6	17.5	17.5	11.6	5.8	

Note: Additional reliability can be obtained through the derating of voltage



#### **Ripple Current/Ripple Voltage**

Permissible AC ripple voltage and current are related to equivalent series resistance (ESR) and the power dissipation capabilities of the device. Permissible AC ripple voltage which may be applied is limited by two criteria:

1. The positive peak AC voltage plus the DC bias voltage, if any, must not exceed the DC voltage rating of the capacitor.

2. The negative peak AC voltage in combination with bias voltage, if any, must not exceed the allowable limits specified for reverse voltage. See the Reverse Voltage section for allowable limits.

The maximum power dissipation by case size can be determined using the table at right. The maximum power dissipation rating stated in the table must be reduced with increasing environmental operating temperatures. Refer to the table below for temperature compensation requirements.

	Temperature Compensation Multipliers for Maximum Ripple Current										
T ≤ 25°C	°C T ≤ 85°C T ≤ 125°C T ≤ 150°C T ≤ 175°C T ≤ 200°C										
1.00	1.00 0.90 0.40 0.30 0.20 0.10										

T = Environmental Temperature

KEMET Case Code	EIA Case Code	Maximum Power Dissipation (P max) mWatts at 25°C w/+20°C Rise
Х	7343-43	165

The maximum power dissipation rating must be reduced with increasing environmental operating temperatures. Refer to the Temperature Compensation Multiplier table for details.

Using the P max of the device, the maximum allowable rms ripple current or voltage may be determined.

 $I(max) = \sqrt{P max/R}$  $E(max) = Z \sqrt{P max/R}$ 

*I = rms ripple current (amperes)* 

E = rms ripple voltage (volts)

P max = maximum power dissipation (watts)

R = ESR at specified frequency (ohms)

Z = Impedance at specified frequency (ohms)



#### **Reverse Voltage**

Solid tantalum capacitors are polar devices and may be permanently damaged or destroyed if connected with the wrong polarity. The positive terminal is identified on the capacitor body by a stripe, plus in some cases a beveled edge. A small degree of transient reverse voltage is permissible for short periods per the below table. The capacitors should not be operated continuously in reverse mode, even within these limits.

Temperature	Permissible Transient Reverse Voltage
25°C	15% of Rated Voltage
85°C	5% of Rated Voltage
125°C	1% of Rated Voltage

#### Table 2 - Land Dimensions/Courtyard

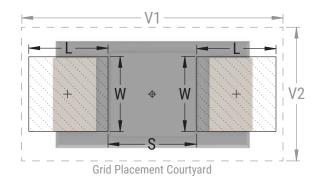
KEMET	Metric Size Code	Density Level A: Maximum (Most) Land Protrusion (mm)			N	Density Level B: Median (Nominal) Land Protrusion (mm)				Density Level C: Minimum (Least) Land Protrusion (mm)						
Case	EIA	W	L	S	V1	V2	W	L	S	V1	V2	W	L	S	V1	V2
<b>X</b> <sup>1</sup>	7343-43	2.55	2.77	3.67	10.22	5.60	2.43	2.37	3.87	9.12	5.10	2.33	1.99	4.03	8.26	4.84

**Density Level A:** For low-density product applications. Recommended for wave solder applications and provides a wider process window for reflow solder processes.

**Density Level B:** For products with a moderate level of component density. Provides a robust solder attachment condition for reflow solder processes. **Density Level C:** For high component desity product applications. Before adapting the minimum land pattern variations the user should perform qualification testing based on the conditions outlined in IPC standard 7351 (IPC-7351).

<sup>1</sup> Height of these chips may create problems in wave soldering.

<sup>2</sup> Land pattern geometry is too small for silkscreen outline.





### **Soldering Process**

KEMET's families of surface mount capacitors are compatible with wave (single or dual), convection, IR, or vapor phase reflow techniques. Preheating of these components is recommended to avoid extreme thermal stress. KEMET's recommended profile conditions for convection and IR reflow reflect the profile conditions of the IPC/J-STD-020D standard for moisture sensitivity testing. The devices can safely withstand a maximum of three reflow passes at these conditions.

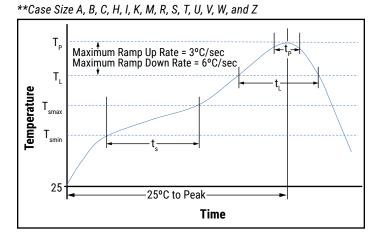
Note that although the X/7343-43 case size can withstand wave soldering, the tall profile (4.3 mm maximum) dictates care in wave process development.

Hand soldering should be performed with care due to the difficulty in process control. If performed, care should be taken to avoid contact of the soldering iron to the molded case. The iron should be used to heat the solder pad, applying solder between the pad and the termination, until reflow occurs. Once reflow occurs, the iron should be removed immediately. "Wiping" the edges of a chip and heating the top surface is not recommended.

During typical reflow operations, a slight darkening of the gold-colored epoxy may be observed. This slight darkening is normal and not harmful to the product. Marking permanency is not affected by this change.

Profile Feature	SnPb Assembly	Pb-Free Assembly		
Preheat/Soak				
Temperature Minimum $(T_{Smin})$	100°C	150°C		
Temperature Maximum (T <sub>Smax</sub> )	150°C	200°C		
Time ( $t_s$ ) from $T_{smin}$ to $T_{smax}$ )	60 – 120 seconds	60 – 120 seconds		
Ramp-up Rate ( $T_L$ to $T_P$ )	3°C/seconds maximum	3°C/seconds maximum		
Liquidous Temperature (T <sub>L</sub> )	183°C	217°C		
Time Above Liquidous (t <sub>L</sub> )	60 – 150 seconds	60 – 150 seconds		
Peak Temperature (T <sub>P</sub> )	220°C* 235°C**	250°C* 260°C**		
Time within 5°C of Maximum Peak Temperature (t <sub>P</sub> )	20 seconds maximum	30 seconds maximum		
Ramp-down Rate $(T_P to T_L)$	6°C/seconds maximum	6°C/seconds maximum		
Time 25°C to Peak Temperature	6 minutes maximum	8 minutes maximum		

Note: All temperatures refer to the center of the package, measured on the package body surface that is facing up during assembly reflow. \*Case Size D, E, P, Y, and X

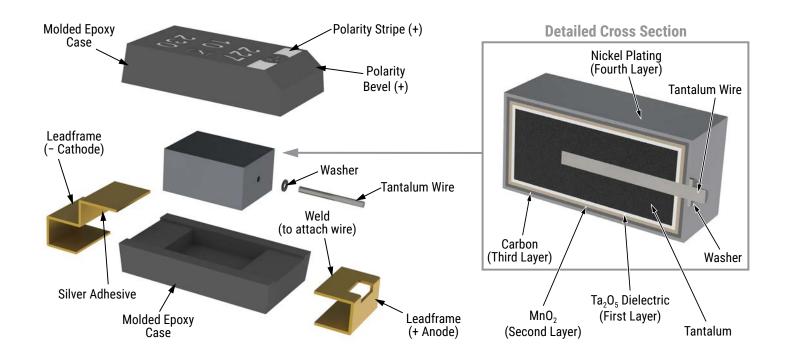


#### Storage

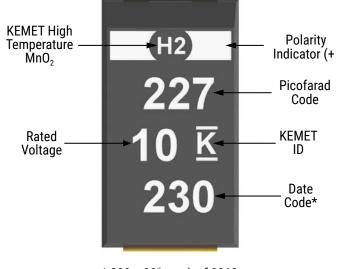
Tantalum chip capacitors should be stored in normal working environments. While the chips themselves are quite robust in other environments, solderability will be degraded by exposure to high temperatures, high humidity, corrosive atmospheres, and long term storage. In addition, packaging materials will be degraded by high temperature–reels may soften or warp and tape peel force may increase. KEMET recommends that maximum storage temperature not exceed 40°C and maximum storage humidity not exceed 60% relative humidity. Temperature fluctuations should be minimized to avoid condensation on the parts and atmospheres should be free of chlorine and sulphur bearing compounds. For optimized solderability chip stock should be used promptly, preferably within three years of receipt.



### Construction



### **Capacitor Marking**

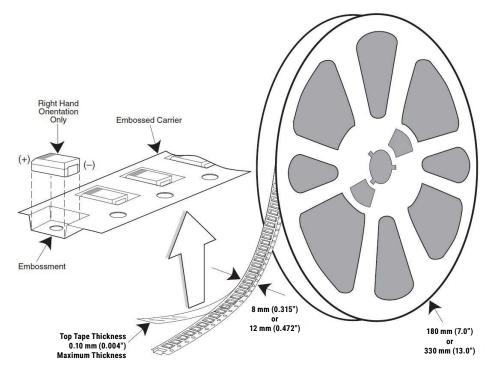


Date (	Code *
1⁵t digit = Last number of Year	2 = 2012 3 = 2013 4 = 2014 5 = 2015 6 = 2016 7 = 2017
2 <sup>nd</sup> and 3 <sup>rd</sup> digit = Week of the Year	01 = $1^{st}$ week of the Year to 52 = $52^{nd}$ week of the Year



#### **Tape & Reel Packaging Information**

KEMET's molded chip capacitor families are packaged in 8 and 12 mm plastic tape on 7" and 13" reels in accordance with *EIA Standard 481*: Embossed Carrier Taping of Surface Mount Components for Automatic Handling. This packaging system is compatible with all tape-fed automatic pick-and-place systems.



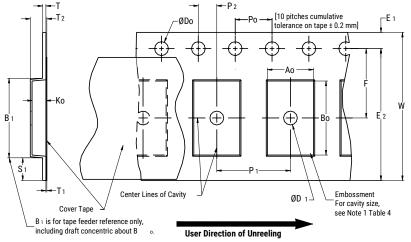
## Table 3 – Packaging Quantity

Case Code		Tape Width (mm)	7" Reel*	13" Reel*	
KEMET	EIA				
S	3216-12	8	2,500	10,000	
Т	3528-12	8	2,500	10,000	
М	3528-15	8	2,000	8,000	
U	6032-15	12	1,000	5,000	
L	6032-19	12	1,000	3,000	
W	7343-15	12	1,000	3,000	
Z	7343-17	12	1,000	3,000	
V	7343-20	12	1,000	3,000	
А	3216-18	8	2,000	9,000	
В	3528-21	8	2,000	8,000	
С	6032-28	12	500	3,000	
D	7343-31	12	500	2,500	
Q	7343-12	12	1,000	3,000	
Y	7343-40	12	500	2,000	
Х	7343-43	12	500	2,000	
E/T428P	7360-38	12	500	2,000	
Н	7360-20	12	1,000	2,500	

\* No C-Spec required for 7" reel packaging. C-7280 required for 13" reel packaging.



## Figure 1 – Embossed (Plastic) Carrier Tape Dimensions



## Table 4 – Embossed (Plastic) Carrier Tape Dimensions

Metric will govern

	Constant Dimensions – Millimeters (Inches)								
Tape Size	D <sub>0</sub>	D <sub>1</sub> Minimum Note 1	E <sub>1</sub>	P <sub>0</sub>	P <sub>2</sub>	R Reference Note 2	S <sub>1</sub> Minimum Note 3	T Maximum	T <sub>1</sub> Maximum
8 mm		1.0 (0.039)			2.0±0.05	25.0 (0.984)			
12 mm	1.5+0.10/-0.0 (0.059+0.004/-0.0)	1.5	1.75±0.10 (0.069±0.004)	4.0±0.10 (0.157±0.004)	(0.079±0.002)	30	0.600 (0.024)	0.600 (0.024)	0.100 (0.004)
16 mm	,	(0.059)	(0.00920.004)	(0.10720.001)	2.0±0.1 (0.079±0.059)	(1.181)	(0.024)	(0.021)	(

Variable Dimensions — Millimeters (Inches)								
Tape Size	Pitch	B <sub>1</sub> Maximum Note 4	E <sub>2</sub> Minimum	F	P <sub>1</sub>	T <sub>2</sub> Maximum	W Maximum	A <sub>0</sub> , B <sub>0</sub> & K <sub>0</sub>
8 mm	Single (4 mm)	4.35 (0.171)	6.25 (0.246)	3.5±0.05 (0.138±0.002)	2.0±0.05 or 4.0±0.10 (0.079±0.002 or 0.157±0.004)	2.5 (0.098)	8.3 (0.327)	
12 mm	Single (4 mm) & Double (8 mm)	8.2 (0.323)	10.25 (0.404)	5.5±0.05 (0.217±0.002)	2.0±0.05 (0.079±0.002) or 4.0±0.10 (0.157±0.004) or 8.0±0.10 (0.315±0.004)	4.6 (0.181)	12.3 (0.484)	Note 5
16 mm	Triple (12 mm)	12.1 (0.476)	14.25 (0.561)	7.5±0.10 (0.295±0.004)	4.0±0.10 (0.157±0.004) to 12.0±0.10 (0.472±0.004)	8.0 (0.315)	16.3 (0.642)	

1. The embossment hole location shall be measured from the sprocket hole controlling the location of the embossment. Dimensions of embossment location and hole location shall be applied independent of each other.

2. The tape, with or without components, shall pass around R without damage (see Figure 4).

3. If S<sub>1</sub> < 1.0 mm, there may not be enough area for cover tape to be properly applied (see EIA Standard 481–D, paragraph 4.3, section b).

4. B, dimension is a reference dimension for tape feeder clearance only.

5. The cavity defined by  $A_{n}$ ,  $B_{n}$  and  $K_{n}$  shall surround the component with sufficient clearance that:

(a) the component does not protrude above the top surface of the carrier tape.

(b) the component can be removed from the cavity in a vertical direction without mechanical restriction, after the top cover tape has been removed.

(c) rotation of the component is limited to 20° maximum for 8 and 12 mm tapes and 10° maximum for 16 mm tapes (see Figure 2).

(d) lateral movement of the component is restricted to 0.5 mm maximum for 8 mm and 12 mm wide tape and to 1.0 mm maximum for 16 mm tape (see Figure 3).

(e) see Addendum in EIA Standard 481–D for standards relating to more precise taping requirements.



### **Packaging Information Performance Notes**

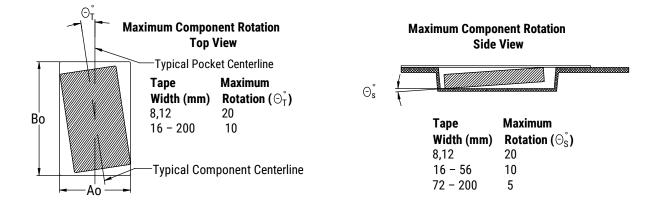
- 1. Cover Tape Break Force: 1.0 Kg minimum.
- 2. Cover Tape Peel Strength: The total peel strength of the cover tape from the carrier tape shall be:

Tape Width	Peel Strength		
8 mm	0.1 to 1.0 Newton (10 to 100 gf)		
12 and 16 mm	0.1 to 1.3 Newton (10 to 130 gf)		

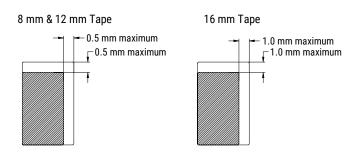
The direction of the pull shall be opposite the direction of the carrier tape travel. The pull angle of the carrier tape shall be  $165^{\circ}$  to  $180^{\circ}$  from the plane of the carrier tape. During peeling, the carrier and/or cover tape shall be pulled at a velocity of  $300 \pm 10$  mm/minute.

**3. Labeling:** Bar code labeling (standard or custom) shall be on the side of the reel opposite the sprocket holes. *Refer to EIA Standards 556 and 624*.

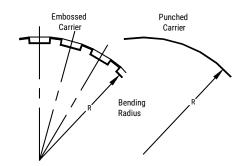
#### Figure 2 – Maximum Component Rotation



### Figure 3 – Maximum Lateral Movement

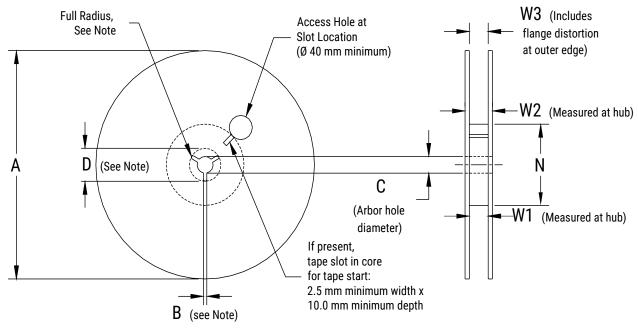


## Figure 4 – Bending Radius





## Figure 5 – Reel Dimensions



Note: Drive spokes optional; if used, dimensions B and D shall apply.

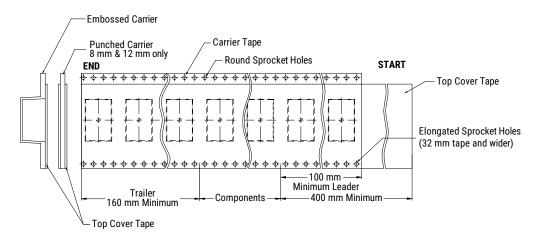
### Table 5 – Reel Dimensions

Metric will govern

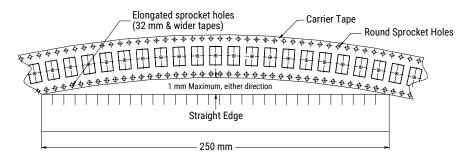
Constant Dimensions – Millimeters (Inches)								
Tape Size	А	B Minimum	С	D Minimum				
8 mm	178±0.20		13.0+0.5/-0.2 (0.521+0.02/-0.008)	20.2 (0.795)				
12 mm	(7.008±0.008) or	1.5 (0.059)						
16 mm	330±0.20 (13.000±0.008)			、				
	Variable Dimensions – Millimeters (Inches)							
Tape Size	N Minimum	W <sub>1</sub>	W <sub>2</sub> Maximum	W <sub>3</sub>				
8 mm		8.4+1.5/-0.0 (0.331+0.059/-0.0)	14.4 (0.567)					
12 mm	50 (1.969)	12.4+2.0/-0.0 (0.488+0.078/-0.0)	18.4 (0.724)	Shall accommodate tape width without interference				
16 mm		16.4+2.0/-0.0 (0.646+0.078/-0.0)	22.4 (0.882)	_				



### Figure 6 – Tape Leader & Trailer Dimensions



### Figure 7 – Maximum Camber





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