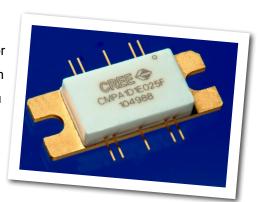


CMPA1D1E025F

25 W, 13.75 - 14.5 GHz, 40 V, Ku-Band GaN MMIC, Power Amplifier

Cree's CMPA1D1E025F is a gallium nitride (GaN) High Electron Mobility Transistor (HEMT) based monolithic microwave integrated circuit (MMIC) on a silicon carbide (SiC) substrate, using a 0.25 µm gate length fabrication process. The Ku Band 25W MMIC is targeted for commercial Ku Band satellite communications applications. It offers high gain and superior efficiency while meets OQPSK linearity required for Satcom applications at 3dB backed off Psat operations. This Ku Band MMIC is available in a 10 lead, 25 mm x 9.9 mm metal/ceramic flanged package.



PN: CMPA1D1E025F Package Type:440208

Typical Performance Over 13.75-14.5 GHz (T_c = 25°C)

Parameter	13.75 GHz	14.0 GHz	14.25 GHz	14.5 GHz	Units
Small Signal Gain	24	24.5	24.5	24	dB
Linear Output Power	24	23	21	20	W
Power Gain	21	21	20	20	dB
Power Added Efficiency	22	20	18	18	%

Note¹: Measured at -30 dBc, 1.6 MHz from carrier, in the CMPA1D1E025F-AMP under OQPSK modulation, 1.6 Msps, PN23, Alpha Filter = 0.2.

Features

- 24 dB Small Signal Gain
- 40 W Typical Pulsed P_{SAT}
- Operation up to 40 V
- 20 W linear power under OQPSK
- Class A/B high gain, high efficiency 50 ohm MMIC Ku
 Band high power amplifier

Applications





Absolute Maximum Ratings (not simultaneous)

Parameter	Symbol	Rating	Units	Conditions
Drain-source Voltage	$V_{\scriptscriptstyle DSS}$	84	V _{DC}	25°C
Gate-source Voltage	V _{GS}	-10, +2	V _{DC}	25°C
Power Dissipation	P _{DISS}	94	W	
Storage Temperature	T _{STG}	-55, +150	°C	
Operating Junction Temperature	$T_{_{J}}$	225	°C	
Maximum Forward Gate Current	I _{GMAX}	10	mA	25°C
Soldering Temperature ¹	T _s	245	°C	
Screw Torque	τ	40	in-oz	
Thermal Resistance, Junction to Case	R _{eJC}	1.5	°C/W	P _{DISS} = 94 W, 85°C
Case Operating Temperature	T _c	-40, +85	°C	CW, P _{DISS} = 94 W

Note:

Electrical Characteristics (Frequency = 13.75 GHz to 14.5 GHz unless otherwise stated; $T_c = 25^{\circ}C$)

Characteristics	Symbol	Min.	Тур.	Max.	Units	Conditions
DC Characteristics ¹						
Gate Threshold	$V_{\rm GS(TH)}$	-3.8	-3.0	-2.3	V	V _{DS} = 10 V, I _D = 18.2 mA
Gate Quiscent Voltage	$V_{\scriptscriptstyle Q}$	-	-2.7	-	V	V _{DS} = 40 V, I _D = 240 mA
Saturated Drain Current ²	I _{DS}	14.6	16.4	-	Α	$V_{DS} = 6.0 \text{ V, } V_{GS} = 2.0 \text{ V}$
Drain-Source Breakdown Voltage	$V_{_{\mathrm{BD}}}$	84	100	-	V	$V_{GS} = -8 \text{ V, } I_{D} = 18.2 \text{ mA}$
RF Characteristics ³						
Small Signal Gain	S21	20.9	24	-	dB	$V_{DD} = 40 \text{ V, } I_{DQ} = 240 \text{ mA, } P_{IN} = -15 \text{ dBm}$
Input Return Loss	S11	-	-7	-6	dB	$V_{DD} = 40 \text{ V}, I_{DQ} = 240 \text{ mA}, P_{IN} = -15 \text{ dBm}$
Output Return Loss	S22	-	-7	-6	dB	$V_{DD} = 40 \text{ V, } I_{DQ} = 240 \text{ mA, } P_{IN} = -15 \text{ dBm}$
Output Mismatch Stress	VSWR	-	-	5:1	Ψ	No damage at all phase angles, $V_{\rm DD}$ = 40 V, $I_{\rm DQ}$ = 240 mA, $P_{\rm OUT}$ = 41 dBm OQPSK

Notes:

¹ Refer to the Application Note on soldering at www.cree.com/products/wireless_appnotes.asp

¹ Measured on-wafer prior to packaging.

² Scaled from PCM data.

³ Measured in the CMPA1D1E025F-AMP



Electrical Characteristics Continued... (T_c = 25°C)

Characteristics	Symbol	Min.	Тур.	Max.	Units	Conditions	
RF Characteristics 1,2,3,4	RF Characteristics 1,2,3,4						
Power Added Efficiency	PAE1	14.5	18.6	-	%	V _{DD} = 40 V, I _{DQ} = 240 mA, Frequency = 13.75 GHz	
Power Added Efficiency	PAE2	12.5	16.4	-	%	V_{DD} = 40 V, I_{DQ} = 240 mA, Frequency = 14.5 GHz	
Power Gain	G _{P1}	19.25	23.3	-	dB	V _{DD} = 40 V, I _{DQ} = 240 mA, Frequency = 13.75 GHz	
Power Gain	G _{P2}	17.75	22.1	-	dB	V_{DD} = 40 V, I_{DQ} = 240 mA, Frequency = 14.5 GHz	
OQPSK Linearity	ACLR1	-	-40	-32	dBc	V_{DD} = 40 V, I_{DQ} = 240 mA, Frequency = 13.75 GHz	
OQPSK Linearity	ACLR2	-	-38	-30.5	dBc	$V_{DD} = 40 \text{ V}, I_{DQ} = 240 \text{ mA},$ Frequency = 14.5 GHz	

Notes:

Electrostatic Discharge (ESD) Classifications

Parameter	Symbol	Class	Test Methodology
Human Body Model	НВМ	1A (> 250 V)	JEDEC JESD22 A114-D
Charge Device Model	CDM	II (200 < 500 V)	JEDEC JESD22 C101-C

¹ Measured in the CMPA1D1E025F-AMP

² Under OQPSK modulated signal, 1.6 Msps, PN23, Alpha Filter = 0.2.

³ Measured at P_{AVE} = 41 dBm. ⁴ Fixture loss de-embedded.



Figure 1. - Small Signal S-parameters CMPA1D1E025F in Test Fixture V_{DD} = 40V, I_{DO} = 240 mA, Tcase = 25°C

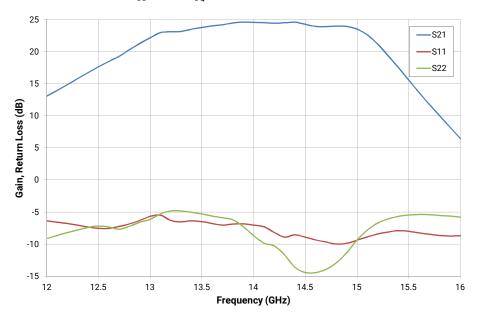


Figure 2. - Modulated @ Spectral Regrowth = -30dBc, 1.6 MHz from Carrier 1.6 Msps OQPSK Modulation $V_{\rm pp}$ = 40 V, $I_{\rm po}$ = 240 mA, Tcase = 25°C

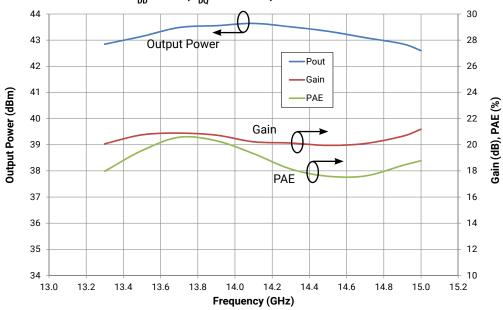




Figure 3. - Spectral Mask @ Average Output Power = 41dBm 1.6 Msps OQPSK Modulation V_{DD} = 40 V, I_{DQ} = 240 mA, Tcase = 25°C

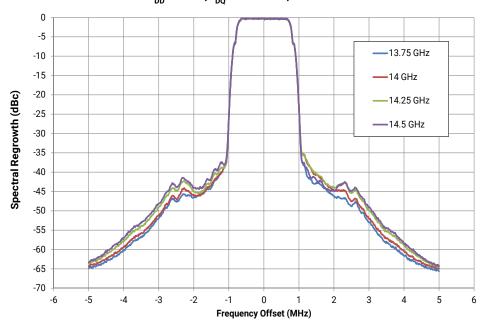


Figure 4. - CMPA1D1E025F Modulated Power Sweep 1.6 Msps OQPSK Modulation V_{DD} = 40 V, I_{DO} = 240 mA, Tcase = 25°C

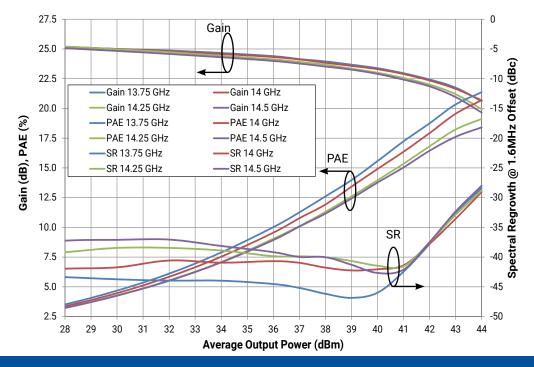




Figure 5. - Modulated Power Sweep 1.6 Msps OQPSK Modulation $V_{DD} = 40 \text{ V, } I_{DO} = 240 \text{ mA, Tcase} = 25^{\circ}\text{C}$

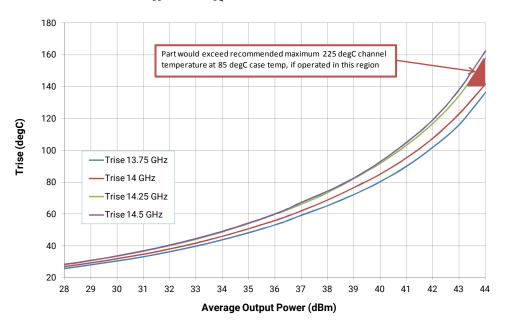


Figure 6. - CMPA1D1E025F Modulated Power Sweep 1.6 Msps OQPSK Modulation V_{DD} = 40 V, I_{DO} = 240 mA, Tcase = 25°C

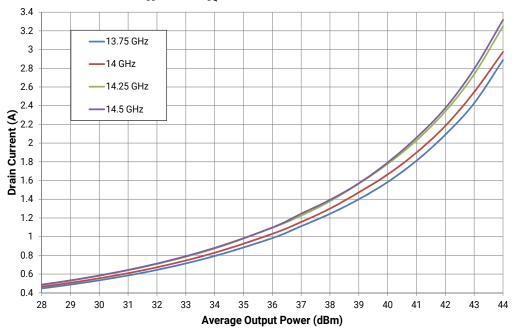




Figure 7. - CMPA1D1E025F Two Tone Power Sweep IMD3 @ 1 MHz Carrier Spacing $V_{\rm DD}$ = 40 V, $I_{\rm DQ}$ = 240 mA, Tcase = 25°C

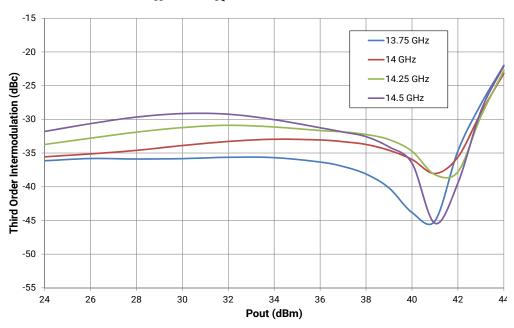


Figure 8. -Two Tone Power Sweep IMD @ 1 MHz Carrier Spacing, 14 GHz $V_{\rm DD}$ = 40 V, $I_{\rm DO}$ = 240 mA, Tcase = 25°C

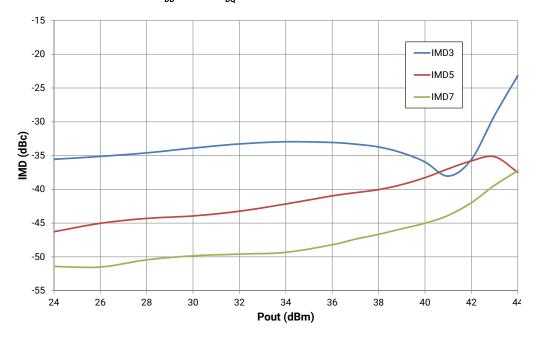






Figure 9. - Two Tone Carrier Spacing Sweep @ 38 dBm Average Ouput Power, 14 GHz $V_{\rm pp}$ = 40 V, $I_{\rm po}$ = 1 A, Tcase = 25°C

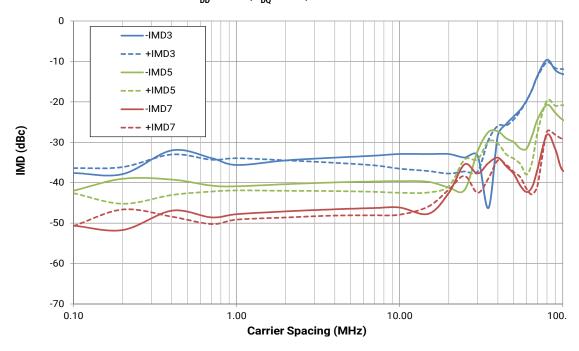


Figure 10. - CW vs. Frequency @ P_{IN} = 23 dBm V_{DD} = 40 V, I_{DQ} = 240 mA, Tcase = 25°C

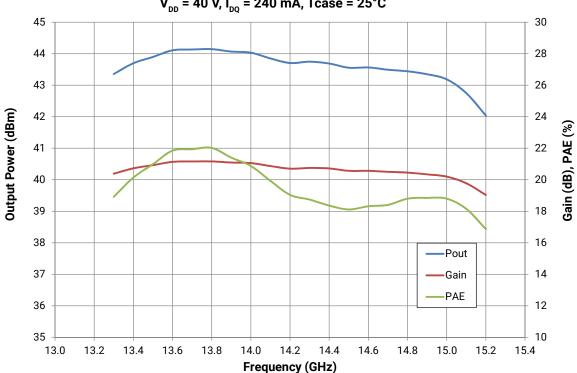




Figure 11. - CW Power Sweep CMPA1D1E025F in Test Fixture V_{DD} = 40V, I_{DO} = 240 mA, Tcase = 25°C

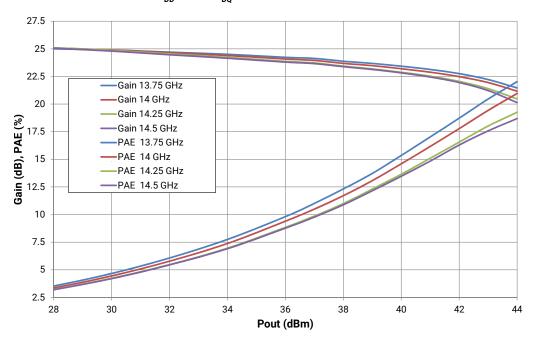


Figure 12. - Pulsed vs. Frequency @ PIN = 23 dBm CMPA1D1E025F in Test Fixture V_{DD} = 40 V, I_{DO} = 240 mA, 100 uS Pulse Width, 10% Duty Cycle, Tcase = 25°C

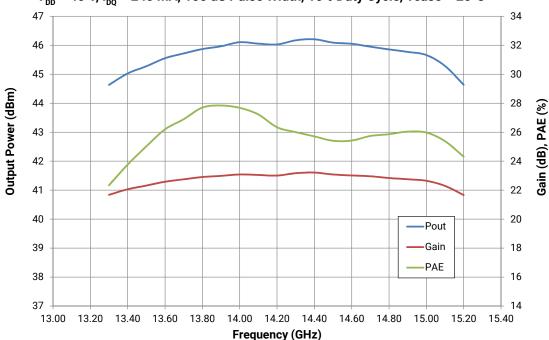




Figure 13. - Pulsed Power Sweep CMPA1D1E025F in Test Fixture 10% Duty, 100 uS Pulse Width V_{DD} = 40V, I_{DO} = 240 mA, Tcase = 25°C

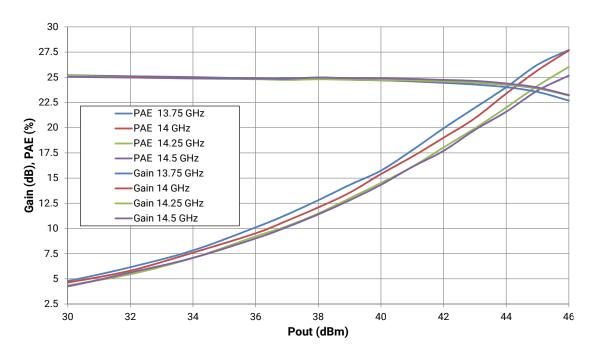
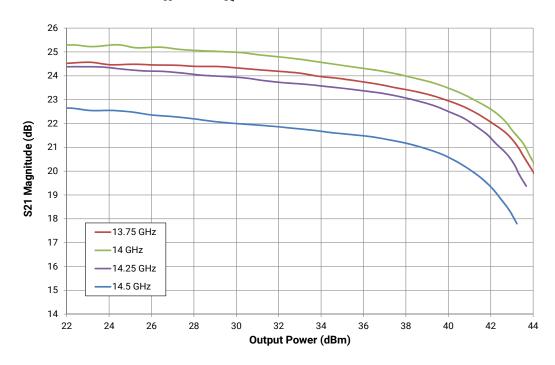
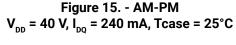


Figure 14. - AM-AM $V_{\rm DD}$ = 40 V, $I_{\rm DQ}$ = 240 mA, Tcase = 25°C







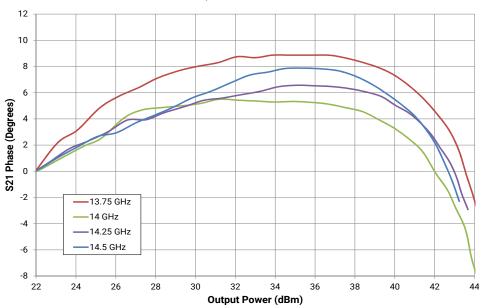


Figure 16. - CMPA1D1E025F Modulated Power Sweep (PAE and Gp) 1.6 Msps OQPSK Modulation, Frequency = 14 GHz V_{DD} = 26-36 V, I_{DQ} = 150 mA, Tcase = 25°C

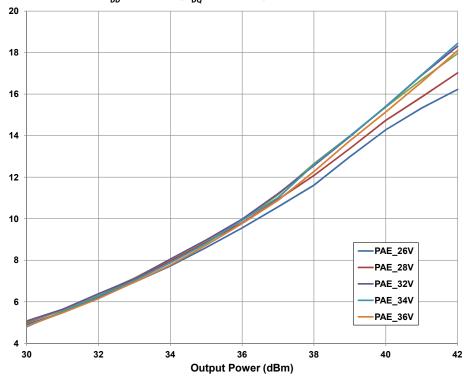




Figure 17. - CMPA1D1E025F Modulated Power Sweep (Gp)
1.6 Msps OQPSK Modulation, Frequency = 14 GHz $V_{DD} = 26-36 \text{ V}, I_{DO} = 150 \text{ mA}, Tcase = 25^{\circ}\text{C}$

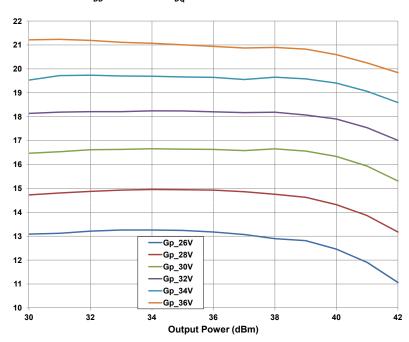
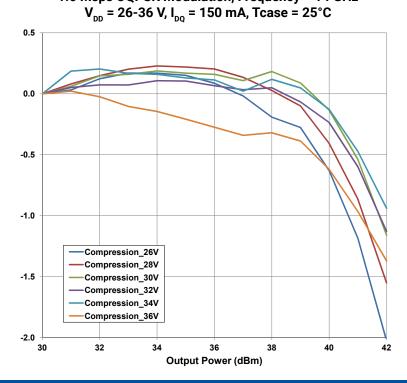
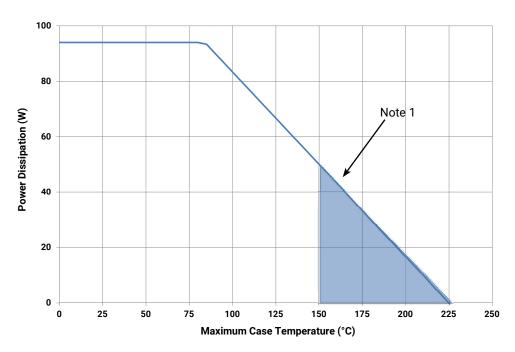


Figure 18. - CMPA1D1E025F Modulated Power Sweep (Gain Compression)
1.6 Msps OQPSK Modulation, Frequency = 14 GHz





CMPA1D1E025F Power Dissipation De-rating Curve



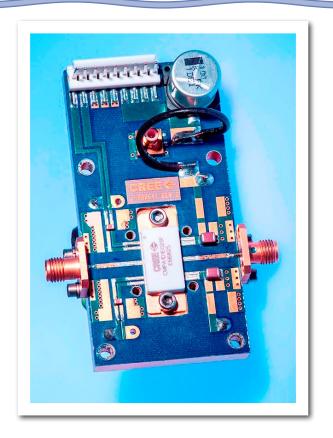
Note 1. Area exceeds Maximum Case Temperature (See Page 2).



CMPA1D1E025F-AMP Demonstration Amplifier Circuit Bill of Materials

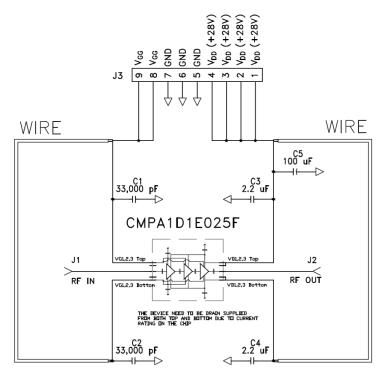
Designator	Description	Qty
C5	CAP ELECT 100UF 80V AFK SMD	1
C1,C2	CAP, 33000PF, 0805,100V, X7R	2
C3,C4	CAP, 2.2UF, 100V, 10%, X7R, 1210	4
J1,J2	CONN, SMA, PANEL MOUNT JACK, FLANGE, 4-HOLE, BLUNT POST, 20MIL	2
J4	CONN, SMB, STRAIGHT JACK RECEPTACLE, SMT, 50 OHM, Au PLATED	1
J3	HEADER RT>PLZ .1CEN LK 9POS	1
W1	WIRE, BLACK, 22 AWG ~ 1.50"	1
W2	WIRE, BLACK, 22 AWG ~ 1.75"	1
W3	WIRE, BLACK, 22 AWG ~ 2.0"	1
	PCB, TEST FIXTURE, TACONICS RF35P, 20 MILS, 440208 PKG	1
	2-56 SOC HD SCREW 1/4 SS	4
-	#2 SPLIT LOCKWASHER SS	4
Q1	CMPA1D1E025F	1

CMPA1D1E025F-AMP Demonstration Amplifier Circuit

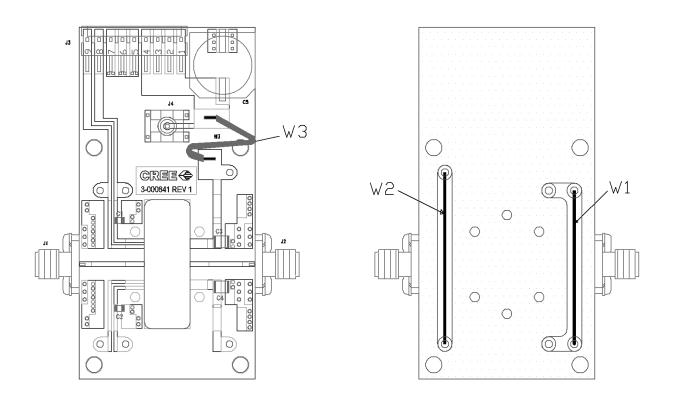




CMPA1D1E025F-AMP Demonstration Amplifier Circuit Schematic

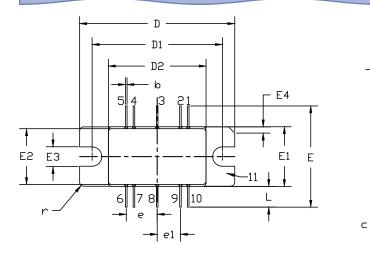


CMPA1D1E025F-AMP Demonstration Amplifier Circuit Outline





Product Dimensions CMPA1D1E025F (Package Type - 440208)



PIN 1: GATE BIAS 6: DRAIN BIAS 2: GATE BIAS 7: DRAIN BIAS 3: RF IN 4: GATE BIAS 9: DRAIN BIAS 5: GATE BIAS 10: DRAIN BIAS 11: SDURCE

NOTES:

200.

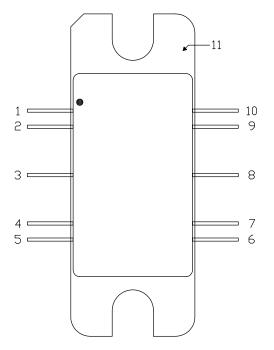
- A1

Α2

- 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M 1994.
- 2. CONTROLLING DIMENSION: INCH.
- 3. ADHESIVE FROM LID MAY EXTEND A MAXIMUM OF 0.020' BEYOND EDGE OF LID.
- 4. LID MAY BE MISALIGNED TO THE BODY OF PACKAGE BY A MAXIMUM OF 0.008' IN ANY DIRECTION.

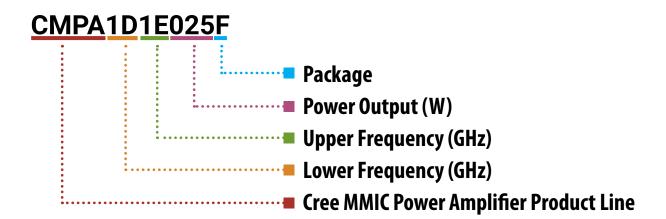
	INC	HES	MILLIN	IETERS	NOTES
DIM	MIN	MAX	MIN	MAX	
Α	0.148	0.168	3.76	4.27	
A1	0.055	0.065	1.40	1.65	
A2	0.035	0.045	0.89	1.14	
b	0.01	TYP	0.254	TYP	10x
С	0.007	0.009	0.18	0.23	
D	0.995	1.005	25.27	25.53	
D1	0.835	0.845	21.21	21.46	
D2	0.623	0.637	15.82	16.18	
Е	0.653	TYP	16.59 TYP		
E1	0.380	0.390	9.65	9.91	
E2	0.355	0.365	9.02	9.27	
E3	0.120	0.130	3.05	3.30	
E4	0.035	0.045	0.89	1.14	45° CHAMFER
е	0.20	O TYP	5.08	TYP	4x
e1	0.15	O TYP	3.81	TYP	4x
L	0.115	0.155	2.92	3.94	10x
r	0.02	5 TYP	.635	TYP	3x

Pin Number	Qty
1	Gate Bias
2	NC
3	RF In
4	NC
5	Gate Bias
6	Drain Bias
7	Drain Bias
8	RF Out
9	Drain Bias
10	Drain Bias
11	Source





Part Number System



Parameter	Value	Units
Lower Frequency	13.75	GHz
Upper Frequency ¹	14.5	GHz
Power Output	25	W
Package	Flange	-

Table 1.

Note¹: Alpha characters used in frequency code indicate a value greater than 9.9 GHz. See Table 2 for value.

Character Code	Code Value	
А	0	
В	1	
С	2	
D	3	
E	4	
F	5	
G	6	
Н	7	
J	8	
K	9	
Examples:	1A = 10.0 GHz 2H = 27.0 GHz	

Table 2.



Product Ordering Information

Order Number	Description	Unit of Measure	lmage
CMPA1D1E025F	GaN HEMT	Each	CREES CORP TO A DE PORTO DE LA CORPENSA DEL CORPENSA DEL CORPENSA DE LA CORPENSA
CMPA1D1E025F-TB	Test board without GaN HEMT	Each	
CMPA1D1E025F-AMP	Test board with GaN HEMT installed	Each	



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